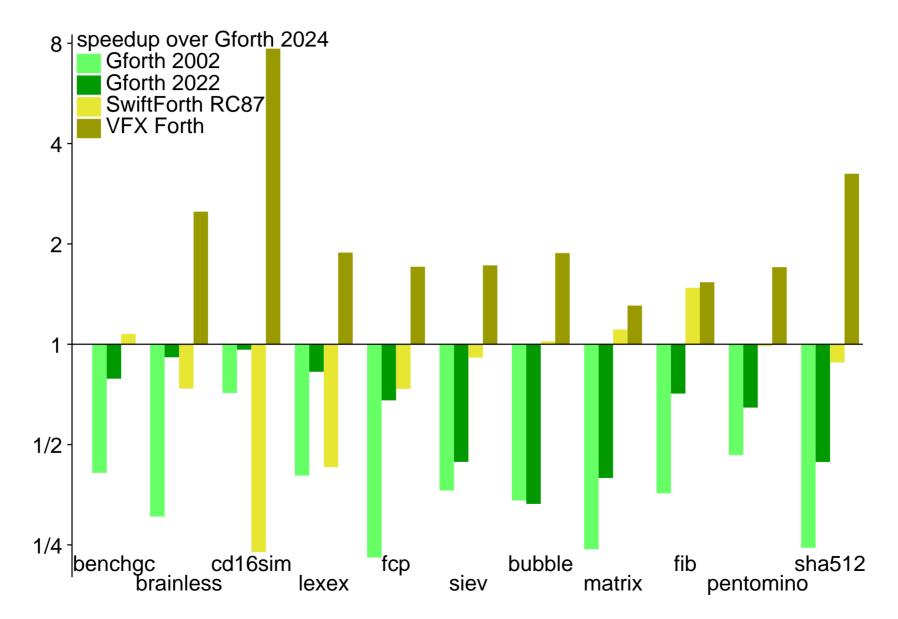
# How to implement words (efficiently)

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### Motivation

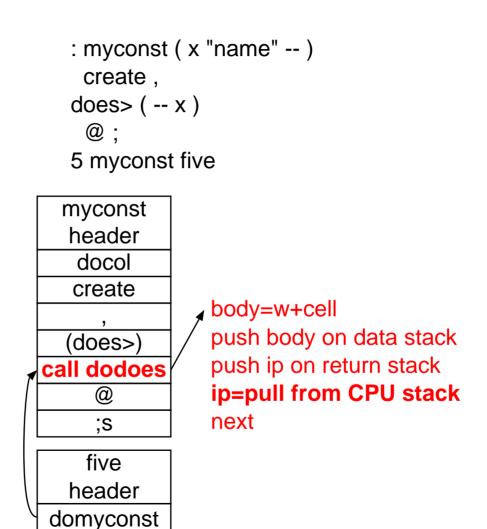


# Call-pull: indirect-threaded origins

# Problem

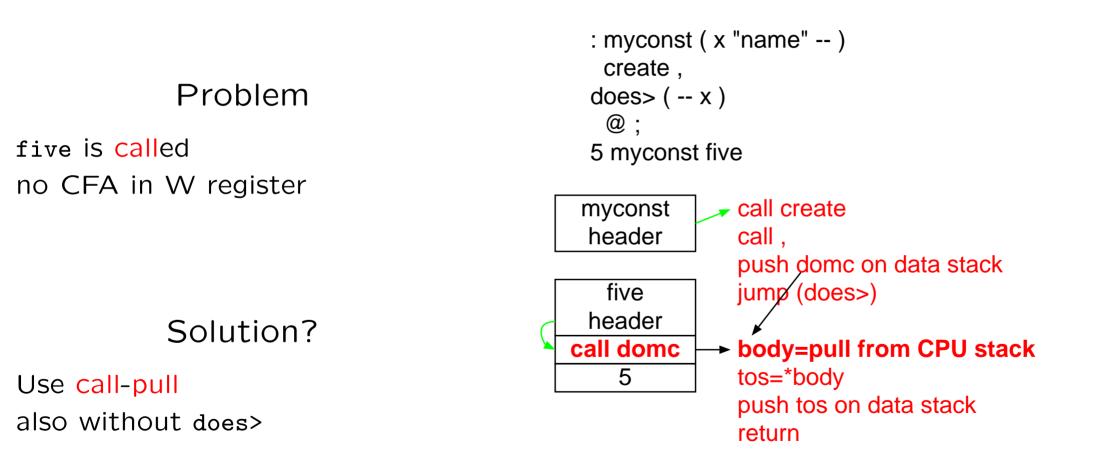
From one cfa/xt get:

- body address
- does-code address
- code address (dodoes)

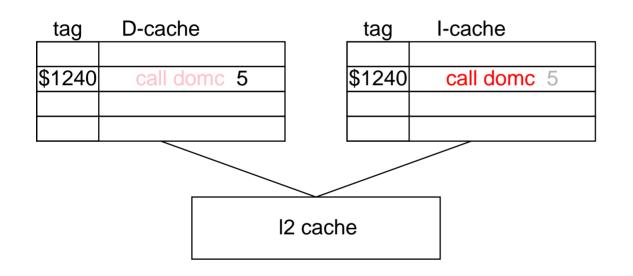


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### Call-pull in native-code systems

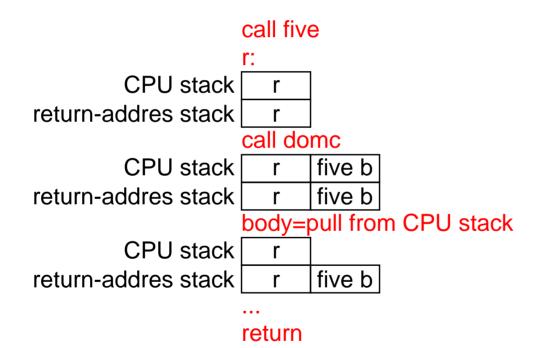


### Performance pitfall: false sharing



- Granularity: cache lines (64B)
- Write invalidates the line in other cache(s)
- Usually D-caches of different cores
- Between I and D cache: on IA-32, AMD64, s390(x)
- Cost for round-trip:  $\approx 400$  cycles on Intel P  $\approx 100$  cycles on Ryzen 5800X
- True sharing at least as bad slow on all architectures

### Performance pitfall: return misprediction



- return address-stack: hardware for branch prediction
- call-pull results in out-of-sync stacks usually one or more branch mispredictions
- Cost:  $\approx$  20–30 cycles per misprediction

# Initial case

: d1 ( "name" )						
create 0 ,			cach	e misses	branch	
does> ( addr )	cycles	inst.	Ι	D	mispred	system
;	8.2	34.0	0.0	0.0	0.0	gforth
d1 z1	9.0	6.6	0.0	0.0	0.0	iforth
: bench-z1-comp ( )	6.4	15.0	0.0	0.0	0.0	lxf
iterations 0 ?do	6.5	14.0	0.0	0.0	0.0	sf RC89
1 z1 +!	434.2	15.0	2.0	2.0	1.0	sf RC87
1 21 7:	7.7	4.6	0.0	0.0	0.0	vfx
loop ;						

- Based on application (CD16sim)
- Slowness due to call-pull

### Does only SwiftForth RC87 have such problems?

: d2 ( "name" )						
create Oe f,			cach	e misses	branch	
does> ( )	cycles	inst.	I		mispred	system
1e dup f@ f+ f! ;	10.4		0.0	0.0	0.0	gforth
d2 z2	449.5	49.6	2.0	2.1	0.0	iforth
: bench-z2-exec ( )	13.5	19.0	0.0	0.0	0.0	lxf
['] z2 iterations 0 ?do	428.3	26.0	2.0	2.0	1.0	sf RC89
	249.5	30.0	2.0	1.0	1.0	sf RC87
dup execute	228.2	16.6	1.0	1.0	1.0	vfx
loop ;						

• call-pull frequent for executeing xts

### Do such problems only occur with does>?

create $x 0$ ,			cach	e misses	branch	
: bench-x-exec ( )	cycles	inst.	Ι	D	mispred	system
['] x iterations 0 ?do	7.0	28.0	0.0	0.0	0.0	gforth
	16.5	49.6	0.0	0.0	0.0	iforth
1 over execute +!	6.0	17.0	0.0	0.0	0.0	lxf
loop drop ;	442.8	24.0	2.0	2.0	1.0	sf
	221.1	17.6	1.0	1.0	1.0	vfx

• Created word implemented with call-pull

#### What about defer and is?

0 constant my0						
defer w ' myO is w			cache	e misses	branch	
: bench-w-comp ( )	cycles	inst.	Ι	D	mispred	system
['] my0 ['] drop	7.0	22.5	0.0	0.0	0.0	gforth
iterations 0 ?do	9.2	19.6	0.0	0.0	0.0	iforth
w over is w	427.0	21.5	2.0	1.0	0.3	lxf
loop	435.9	19.5	2.7	2.0	1.0	sf
•	205.3	11.1	1.0	1.0	0.5	vfx
2drop ;						

- True sharing on lxf thanks to jump-patching
- False sharing on SwiftForth and VFX thanks to call-pull

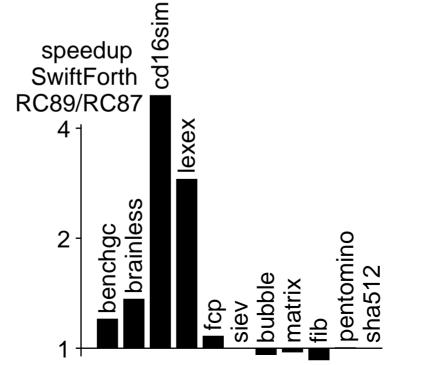
#### What about defer without is?

0 constant my0						
defer w ' myO is w			cache	e misses	branch	
: bench-w-nois-comp ( )	cycles	inst.	Ι	D	mispred	system
iterations 0 ?do	8.4	35.0	0.0	0.0	0.0	gforth
	15.5	42.6	0.0	0.0	0.0	iforth
w drop	6.0	12.0	0.0	0.0	0.0	lxf
loop ;	29.4	16.0	0.0	0.0	1.0	sf
' z1 is w bench-w-nois-comp	27.2	11.6	0.0	0.0	1.0	vfx

- Return mispredictions in SwiftForth and VFX thanks to call-pull
- Similar for other uses of call-pull without writes

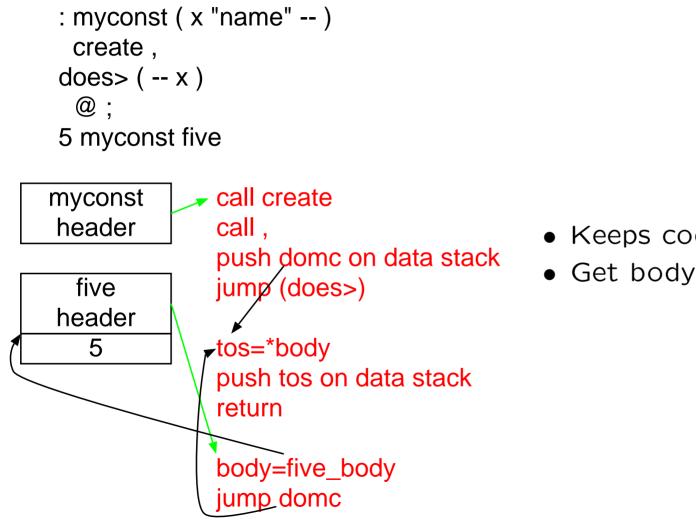
But what about applications?

- How do you know that an application is affected?
- Compare I-cache misses with other Forth systems
- Compare branch mispredictions with other Forth systems
- Implement words using techniques without these performance pitfalls Compare your Forth before and after



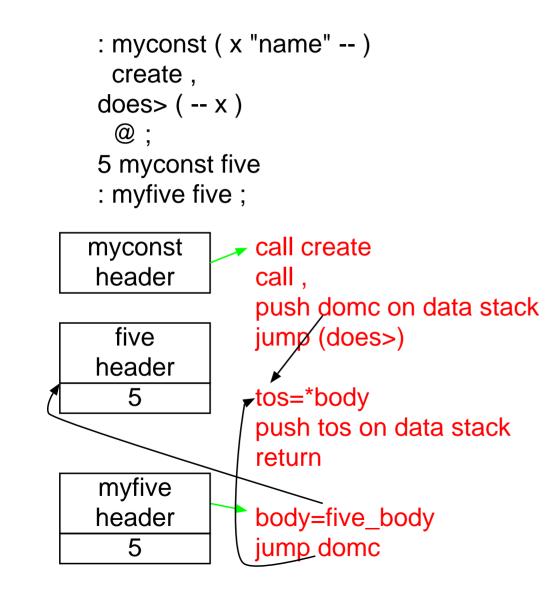
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# How to avoid call-pull? (1) Trampolines



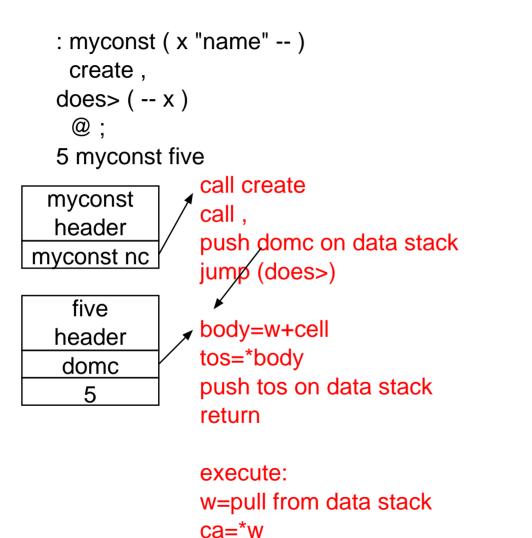
- Keeps code separate from data
- Get body address without pull

# How to avoid call-pull? (2) Intelligent compile,



- Compile address of body, then call to doer call is tail-call optimized in myfive
- Keeps code separate from data
- Get body address without pull
- Can be used to generate trampoline
- Does not help for execute or defer

# How to avoid call-pull? (3) Code field



jump ca

- Set W to CFA, then perform indirect call
- Fine for execute and deferred words
- Slow for naively compile,d code but intelligent compile, avoids that

### How to implement deferred words

- Just use a data field and an indirect jump
- Don't patch native-code jumps

# Conclusion

- Call-pull slowdowns false sharing (100+ cycles) return mispredictions (20-30 cycles)
- Big slowdowns in microbenchmarks
- How much in applications?
  You know it when you fix it
- Avoiding call-pull is possible trampolines (used in lxf) intelligent compile, (used everywhere) code field
- Use data field for deferred words
- Microbenchmarks: http://www.euroforth.org/ef24/papers/ertl.fs